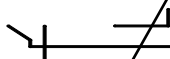


### Internal Circuit



### Features

- International standard package
- High Surge Capability
- Simple Mounting

### Blocking - Off State

TYPE		VDRM/VRRM	VDSM/VRSM	Units
MT200C(A/K)28T3	MT200C(A/K)B28T3	2800	2900	V
MT200C(A/K)30T3	MT200C(A/K)B30T3	3000	3100	V
MT200C(A/K)32T3	MT200C(A/K)B32T3	3200	3300	V
MT200C(A/K)34T3	MT200C(A/K)B34T3	3400	3500	V
MT200C(A/K)36T3	MT200C(A/K)B36T3	3600	3700	V

### Maximum Ratings

Symbol	Conditions	Values	Units
$I_{TAV}$	Sine 180°; $T_c=85$	200	A
$I_{TSM}$	$T_{VJ}=125$ , $t=10ms$ , sine	6000	A
$I^2t$	$T_{VJ}=125$ , $t=10ms$ , sine	180000	A <sup>2</sup> s
Visol	a.c.50HZ;r.m.s.;1min, $I_{iso}:2mA(MAX)$	4000	V
$T_{vj}$		-40 to 125	°C
$T_{stg}$		-40 to 125	°C
$M_t$	To terminals(M8)	12±15%	%
$M_s$	To heatsink(M6)	6±15%	%
di/dt	$T_{VJ}=T_{VJM}$ , $V_{DM} \leq 2/3V_{DRM}$ , $I_{GM}=1.5A$ , $t_r=0.5\mu s$	100	A/μs
dv/dt	$T_{VJ}=T_{VJM}$ , $2/3V_{DRM}$ , linear voltage rise	100	V/μs
Weight	Module(Approximately)		g

### Thermal Characteristics

Symbol	Conditions	Values	Units
$R_{th(j-c)}$	per chip		°C/W
$R_{th(c-h)}$	per chip		°C/W

